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FIG.17 PRIOR ART

STRUCTURAL DIAGRAM ILLUSTRATING DATA PROCESSOR FOR READING DATA FROM A MEMORY MEDIUM OF RELATED ART

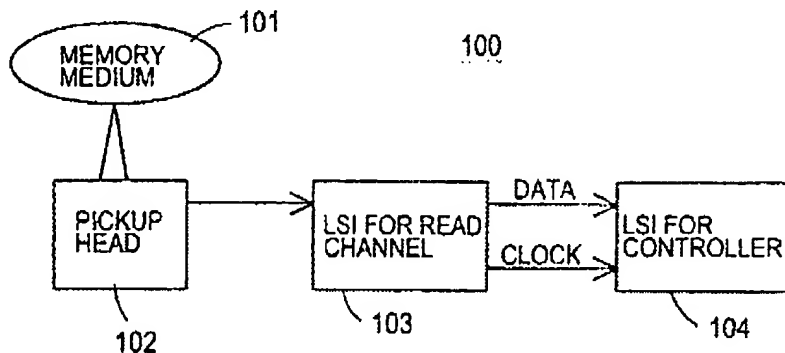
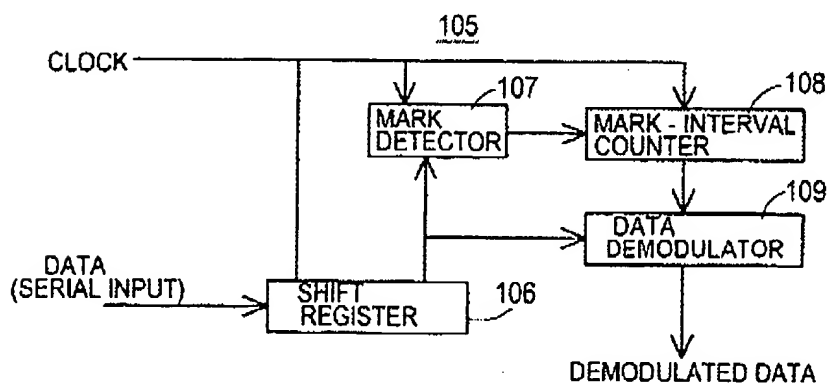
**FIG.18 PRIOR ART**

DIAGRAM ILLUSTRATING A STRUCTURE IN CONTROLLER OF RELATED ART FOR PROCESSING THE SERIAL DATA TRANSFERRED FROM READ CHANNEL UNIT



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FIG.17 PRIOR ART

STRUCTURAL DIAGRAM ILLUSTRATING DATA PROCESSOR FOR READING DATA FROM A MEMORY MEDIUM OF RELATED ART

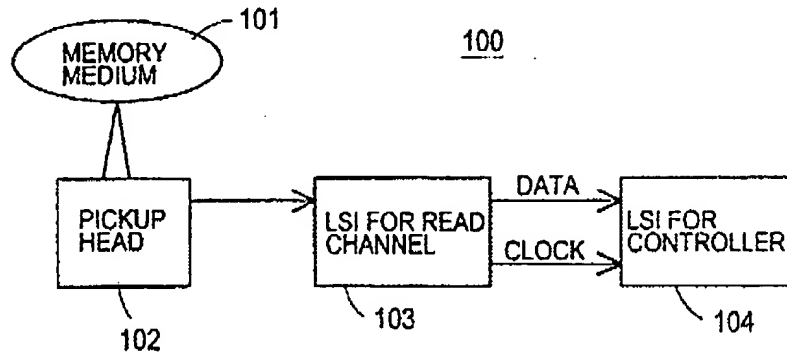
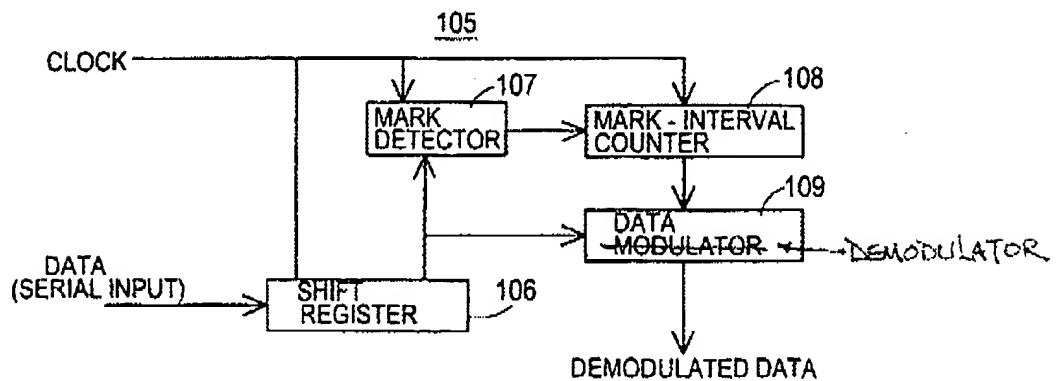
**FIG.18 PRIOR ART**

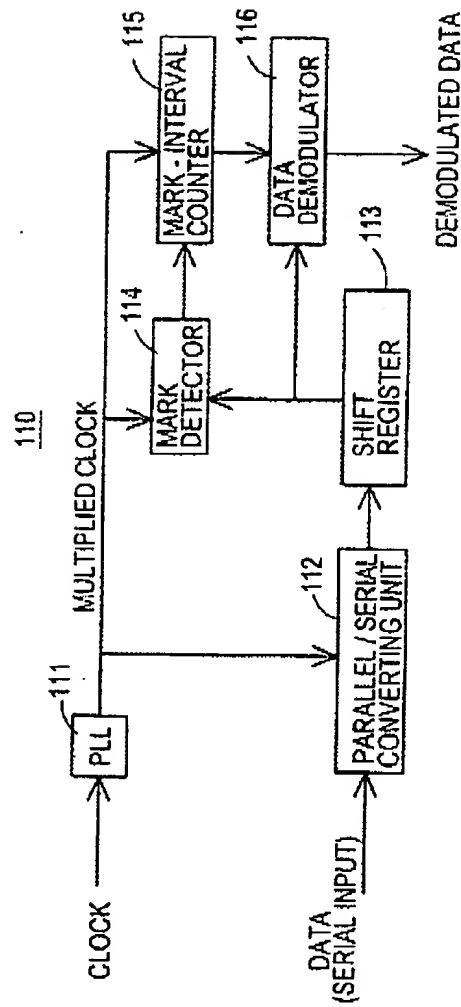
DIAGRAM ILLUSTRATING A STRUCTURE IN CONTROLLER OF RELATED ART FOR PROCESSING THE SERIAL DATA TRANSFERRED FROM READ CHANNEL UNIT



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FIG.19 PRIOR ART

DIAGRAM ILLUSTRATING A STRUCTURE IN CONTROLLER OF RELATED ART (IN PARALLEL CONDITION)



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FIG.19 PRIOR ART

DIAGRAM ILLUSTRATING A STRUCTURE IN CONTROLLER OF RELATED ART (IN PARALLEL CONDITION)

